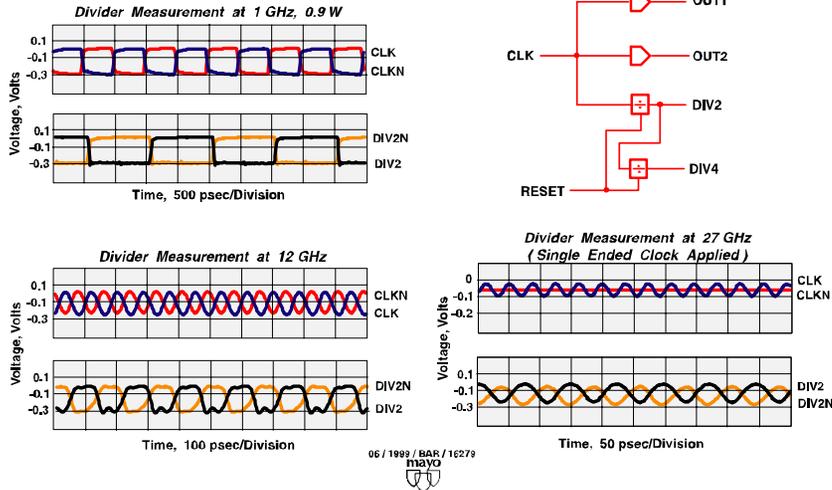
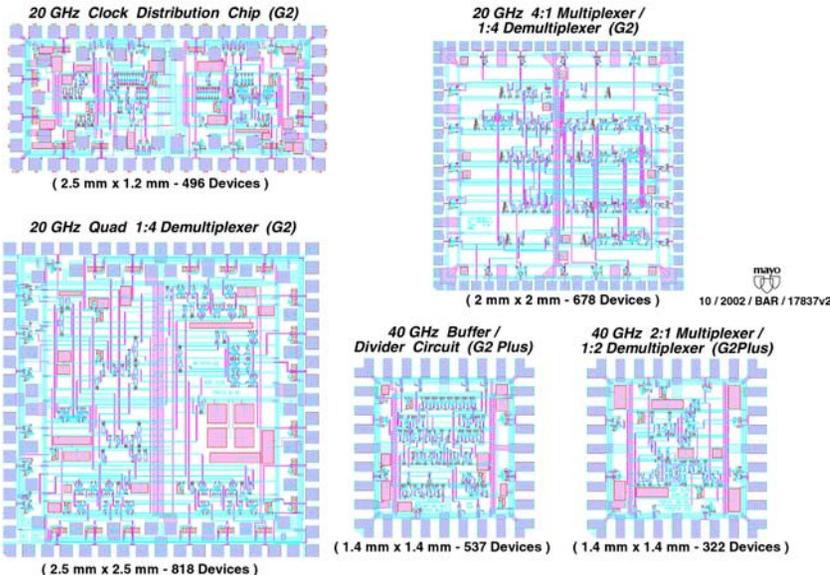


**MAYO DIVIDER CIRCUIT IMPLEMENTED IN HRL LABS InP SCALED HBT TECHNOLOGY**  
 ( HP70843A 12 GHz BERT Source; HP83650B 50 GHz Swept Signal Generator;  
 TEK11801 Oscilloscope; GGB MCW-7-2062 Probes;  
 Mask HBT44; Fabrication Completed April 1999 )



**LAYOUT OF MAYO CIRCUITS**  
 IMPLEMENTED IN HRL InP HBT G2 AND G2PLUS TECHNOLOGY  
 ( Received From Fabrication July 2002 )



## GOALS, OBJECTIVES AND MAIN TECHNICAL APPROACH

Collaborate with other TFAST team members on development of integrated circuit technologies

Provide the sponsor with an unbiased design, test and evaluation facility for emerging advanced InP devices

## MAJOR IMPACT OF TECHNOLOGY

The TFAST technology will enable high data rate mixed signal circuits for DoD applications.

Mayo's typical role is to demonstrate potential uses of new technologies through design and test of circuits of interest to the military use community.