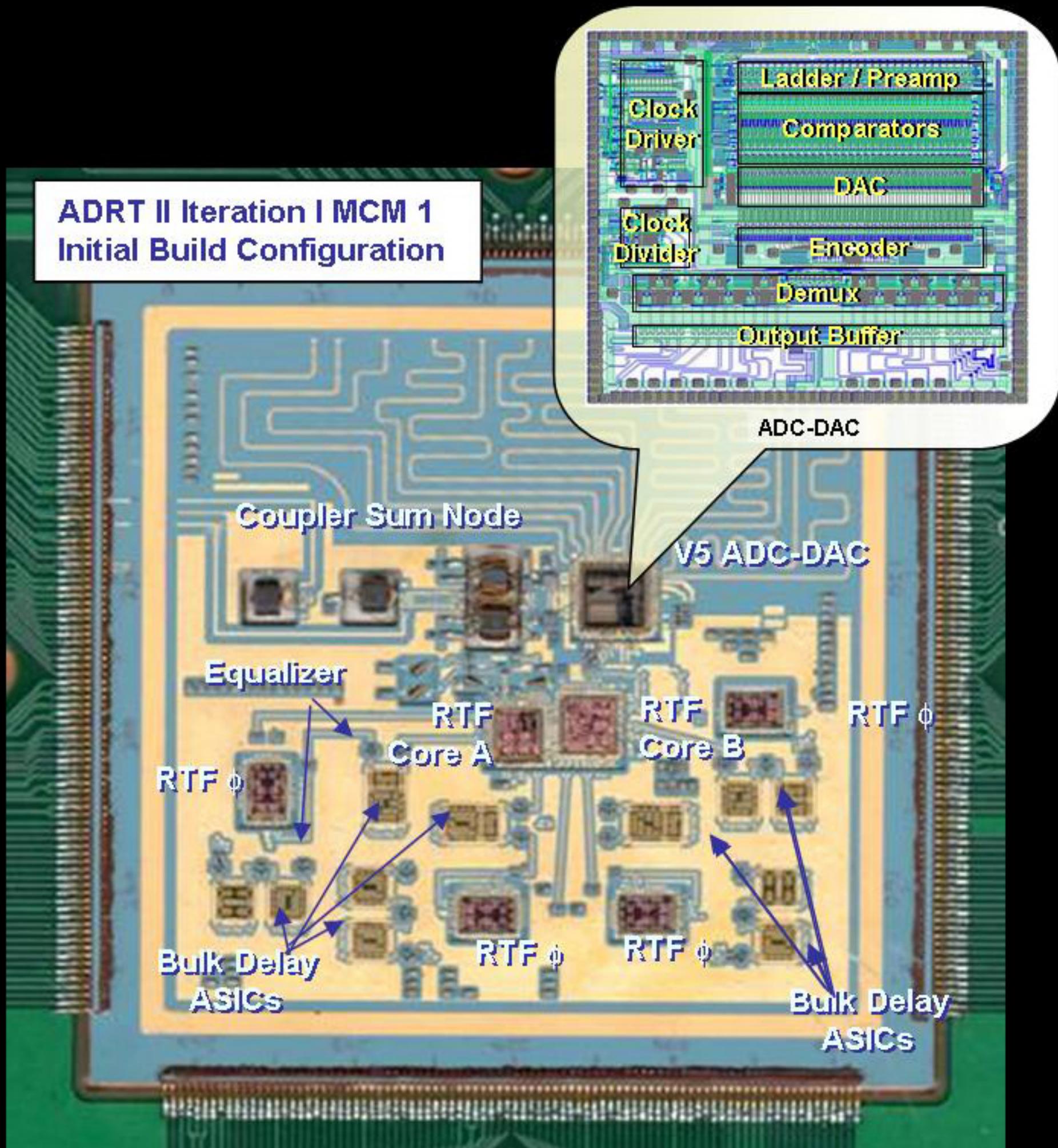


ADRT II Iteration I MCM 1 Initial Build Configuration



First generation implementation of ADR technology using a multichip module (MCM) populated with $\Sigma\Delta$ modulators, recursive transversal filters (RTFs), and custom, application specific integrated circuits (ASICs).